

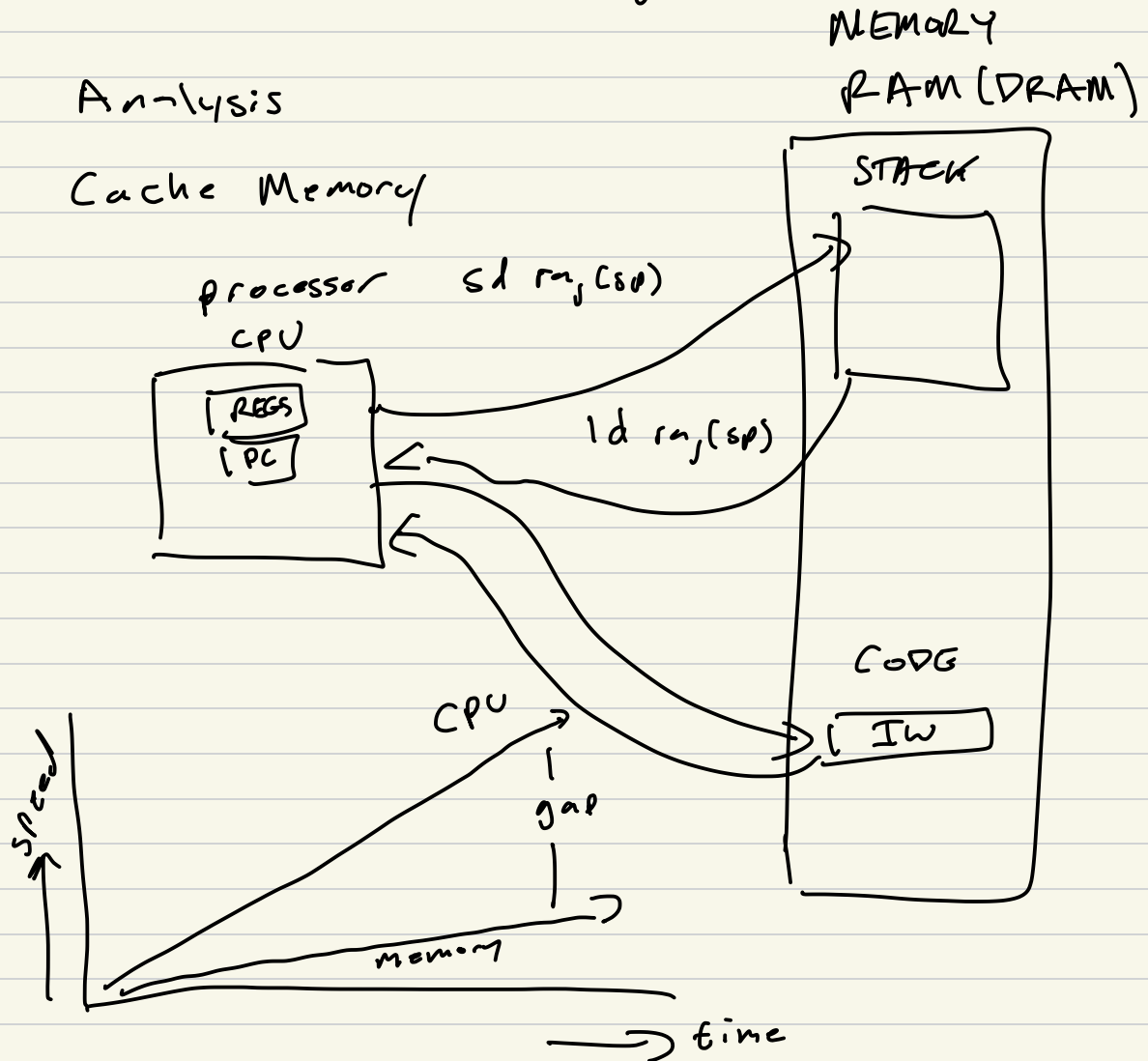
CS315-01 Lab Lab06 Analysis Cache Memory

Lab06 Q1A
sign-extend ()

Project04 - call vs jal

Analysis

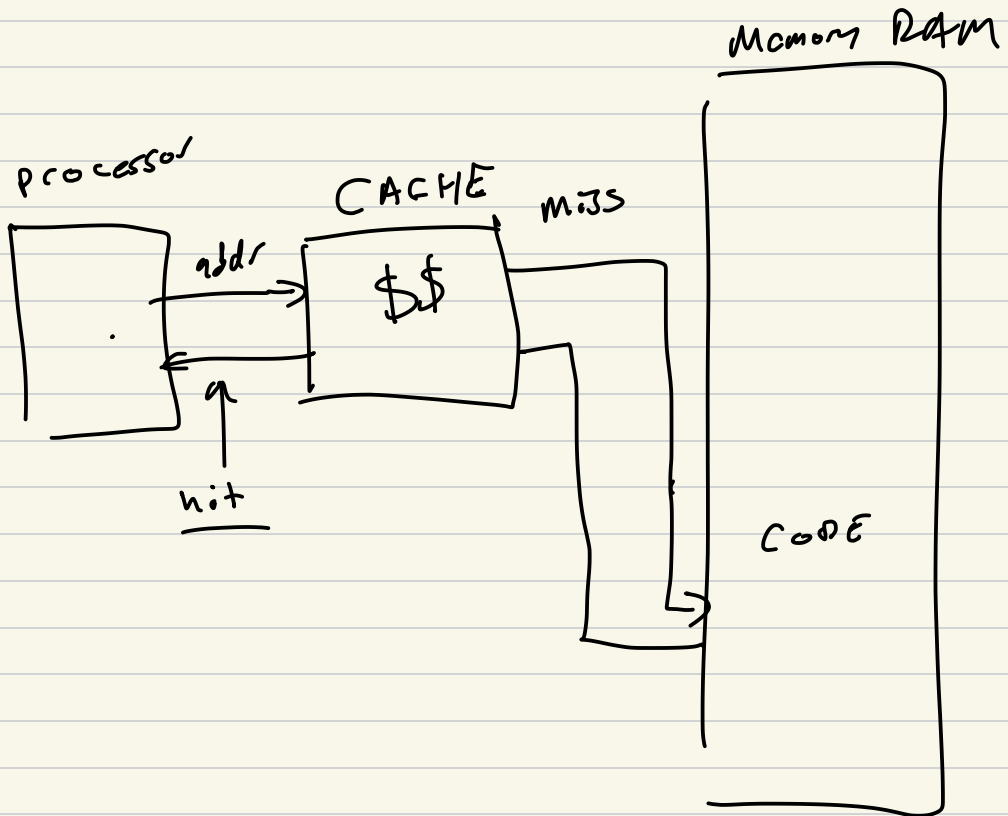
Cache Memory



Two types of RAM

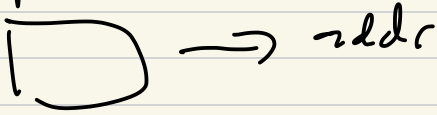
SRAM
Static
random
access
memory

DRAM
dynamic
random
access
memory



Cache Questions

processor



- 1) How to find $\text{addr}(\text{data})$ in the cache?
- 2) How do we know $\text{addr}(\text{data})$ is in the cache?
- 3) Which data do we remove from the cache when full?

Principles of Locality

Spatial

Temporal

Types of Caches

Direct Mapped Cache

Fully Associative Cache

Set Associative Cache